

Digital Amplifier

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The present invention relates to the field of digital amplifiers, which directly convert digital signals to a power output. In detail, the present invention relates to a digital amplifier for converting an input signal to a power output, to a digital amplifier system for converting a plurality of input signals to a plurality of power
10 outputs, to a compensation circuit for connection between a power supply and a class D amplifier and to a method of operating a class D amplifier.

Several types of power-amplifier output stages have been developed. Conveniently, they were labeled as, e.g. class A amplifiers, class B amplifiers and class C amplifiers. More recently, class D amplifiers appeared. Class A, B, AB, and D are
15 common in low-frequency audio designs and have some applications in other areas, such as servo-motor drives and RF amplification. Class C, class E and F types are usually only used in RF applications.

20 In recent years, in particular the class D amplifiers have become increasingly popular due to their drastically improved efficiency in contrast to the generally employed class AB linear amplification technology. Class D amplifiers are generally described in Carsten Nielsen, "High Fidelity PWM based Amplifier Concept for Active Speaker Systems with a very low Energy Consumption", (100th AES
25 Convention, Copenhagen, May 1996, pre-print 4259), which is hereby incorporated by reference.

The development of class D amplifiers represents an effort to improve amplifier efficiency. Similar in scheme to a switching regulator, a class D amplifier, pulse-width-modulates the audio-input signal with a higher frequency square wave so
30 that audio-signal information becomes the variations in pulse-width of the modulation signal. This modulation signal feeds a set of half bridge switches, usually called H-

bridges, and each H-bridge consists of two power MOSFETs. Unlike with class A or B structures, the amplifier load or the loudspeaker is placed across the legs of the bridge instead of from the output to the ground. This configuration allows the amplifier to reproduce low-frequency signals as low as 20 Hz without requiring bipolar power supplies or without introducing a DC offset in the output.

In spite of becoming more and more popular, class D audio amplifiers are known for their poor distortion characteristics. Attempts were made to filter the modulated output to remove high-frequency signals and recover the amplified input signals. Filter configurations such as two-pole Butterworth filters, two-pole Chebyshev or two-pole Bessel filters are known which either did not deliver satisfying results or caused significant efforts and costs.

It is an object of the present invention to avoid distortions in digital amplifiers.

As used herein, the term "digital amplifier" applies to an amplifier which directly converts to a power output.

According to an exemplary embodiment of the present invention, the above object may be solved with a digital amplifier for converting an input signal to a power output comprising a bridge circuit with at least one pair of switches and a supply ripple pre-compensation circuit. The supply ripple pre-compensation circuit is arranged for compensating voltage ripples on a supply voltage supplied to the bridge circuit. According to an aspect of the present invention the supply ripple pre-compensation circuit compensates the voltage ripples on the supply voltage based on the input signal.

According to an aspect of the present invention it has been found out that a significant portion of the distortions of the digital amplifier is caused by ripples in the supply voltage of the digital amplifier. In this respect it has to be noted that with regard to this application, the term "ripples" is not limited to 100 Hz ripples or the like, but refers to disturbances, unwanted waviness and ripples, e.g. within a 20 KHz band width of the output signal of an audio amplifier. Also, the term "compensation" includes any suppression or equalization. In particular, in the case of a class D amplifier, it has been found out that one of the important distortion sources in feed forward class - D

amplifiers is the supply voltage ripple at the full bridge caused by voltage drops due to the load current changes. Amplifiers with feedback, such as for example class A-B amplifiers inherently have excellent power supply ripple rejection. However, such conventional class A-AD amplifying technology cannot be applied to fully digital audio amplifiers without any feedback.

Advantageously, the digital amplifier according to the present invention may be applied to fully digital audio amplifiers without any feedback and ensures improved total harmonic distortion figures due to supply ripple effects.

Advantageously, due to the supply ripple pre-compensation based on the input signal, inputs can be delivered to the bridge circuit load just in time of its demand, provided that this demand is defined by the input signal. Advantageously, this not only ensures a clean supply voltage with minimal ripple, but also means that an energy storage of the power supply can be reduced in relation to known solutions.

According to an exemplary embodiment of the present invention as set forth in claim 2, the digital amplifier includes a filter for filtering the input signal, which is an audio signal. According to this exemplary embodiment of the present invention, only a part of a frequency content of the audio signal is fed forward for pre-compensating ripples on the supply voltage. Such band width limited feed forward signal injection is based on the assumption that most audio signal content is in the low-frequency range of the signal. Therefore, while ensuring that the primary part and most important part of the audio signal content is fed forward, the audio signal can be previously low pass filtered to ease the power supply controller without having a loss of performance.

According to another exemplary embodiment of the present invention as set forth in claim 3, the supply ripple pre-compensation circuit is one of a digital controller and an analog control circuit and the digital amplifier is a class D amplifier, which allows for a very effective compensation of the ripples and therefore for a clean and undisturbed output signal.

According to another exemplary embodiment of the present invention as set forth in claim 4, a digital amplifier system is provided for converting a plurality of input signals to a plurality of power outputs. According to this exemplary embodiment of the present invention, the above described ripple pre-compensation scheme is applied

to more than one channel in a multi-channel application (e.g. left and right sides in a stereo amplifier). This exemplary embodiment of the present invention is based on the assumption that the low frequency content is common to all channels. Thus, satisfying results may be achieved by low pass filtering only one channel of the channels and
5 using the low pass filtered signal of this one channel for pre-compensation of all amplified channels.

According to further exemplary embodiments of the present invention as set forth in claims 5 and 6, a plurality of input signals such as different channels of an audio signal are individually squared and then added up and input to the pre-
10 compensation circuit for performing the supply ripple pre-compensation for the plurality of output channels. The firstly squared and then added-up signal represents the total energy which is drawn from the power supply and therefore can be used for the above described pre-compensation.

Further exemplary embodiments of the present invention as set forth in
15 claims 7 and 8 provide for a compensation circuit for connection between a power supply and a class D amplifier applying the above described pre-compensation where the supply ripple pre-compensation circuit compensates the voltage ripples on the supply voltage based on the input signals. Advantageously, this compensation circuit may be used in already existing circuit configurations and allows for the provision of a
20 clean supply voltage to the bridge of the class D amplifier.

Claims 9, 10 and 11 provide for a method of operating a class D amplifier which converts an input signal to a power output which allows and operation of the class D amplifier with reduced distortions.

It may be seen as the gist of an exemplary embodiment of the present
25 invention that ripples on a supply voltage to digital amplifiers are compensated for on the basis of information in the input signal. In case of audio signals, the content of the audio signal corresponds or defines the load demand of the digital amplifier. This not only ensures a clean supply voltage with minimal ripple, but also means that advantageously the energy storage of the power supply can be reduced.

30 These, and other aspects of the present invention are apparent and will be elucidated with reference to the embodiments described hereinafter and with reference to the following drawings:

Fig. 1 shows a schematic diagram of a first exemplary embodiment of a digital amplifier according to the present invention.

5 Fig. 2 shows a simplified circuit diagram of an exemplary embodiment of a compensation circuit according to the present invention as it may be used in the digital amplifiers depicted in Fig. 1, 3 and 4.

Fig. 3 shows a schematic diagram of a first exemplary embodiment of a digital amplifier system according to the present invention.

10 Fig. 4 shows a schematic diagram of a second exemplary embodiment of a digital amplifier system according to the present invention.

In the following, exemplary embodiments of the present invention will
15 be described with reference to the figures. The digital amplifiers depicted in Figs. 1, 3 and 4 are class D amplifiers, having a full bridge, usually referred to as H-bridge. In spite of the fact that the present invention will be described with reference to exemplary embodiments of class D amplifiers, it is apparent to the skilled person that the present invention is not limited to class D amplifiers, but is applicable to any kind of digital
20 amplifier, where ripples or unwanted disturbances in the power supply voltage cause problems such as distortions in the output signal of the amplifier.

Fig. 1 shows a simplified schematic block diagram of a first exemplary embodiment of a digital amplifier according to the present invention for converting an input signal such as an audio signal to a power output. Reference numeral 2 in Fig. 1
25 designates an audio source which outputs an audio signal to a modulator 4. The modulator 4 pulse-width modulates the audio signal with a higher frequency square wave, such that the signal output at the output of the modulator 4 includes the audio-signal information as variations in pulse-width of the modulated signal. This modulated signal feeds a set of half bridge switches, usually referred to as H-bridges 6. Usually,
30 each H-bridge consists of two power MOSFETs. The output of the bridge 6 is fed into a filter for filtering the signal before the output signal is applied to a loudspeaker 10. Unlike with class A or B structures, the amplifier load, i.e. the loudspeaker 10 is

connected across to the legs of the bridge 6 from the output to the ground. It has to be noted that the present invention may also be implemented in class D half bridge architectures. In case the present invention is applied to class D half bridge architectures, the speaker is then connected to a half supply by means of a series
5 capacitance.

Reference numeral 12 designates a supply ripple pre-compensation circuit which input is connected to the output of the audio source 2 and which output is connected to a power supply 14. The power supply 14 feeds the power supply voltage via a power supply port 16 to the switches of the bridge 6. The supply ripple pre-
10 compensation circuit 12 referred to in Fig. 1 as "pre-compensation" is configured for suppressing or compensating voltage ripples on the supply voltage supplied from the power supply 14 via the supply port 16 to the bridge 6. The supply ripple pre-compensation circuit according to an exemplary embodiment of the present invention, may be one of a digital controller such as a DSP controller and an analog control circuit.

15 Fig. 2 shows a simplified circuit diagram of a supply ripple pre-compensation circuit according to an exemplary embodiment of the present invention, as it may be used in the digital amplifiers or digital amplifier systems depicted in Figs. 1, 3 and 4. In Fig. 2, reference numerals 20 and 22 designate switches of the full bridge of the class D amplifier, which are realized by means of MOSFETs.

20 The circuit depicted in Fig. 2 is a buck converter with an audio signal injection for supply ripple pre-compensation according to the present invention. The depicted circuit may be realized by means of a digital circuit such as a DSP.

The power supply not depicted in Fig. 2 provides the voltage v_0 to the circuit. The source of the switch 20 and the drain of the switch 22 are connected to an
25 inductivity 24, which is connected to the supply output voltage v_0 . A capacity 26 is provided between the supply output voltage v_0 of the power supply and the source of the switch 22. The supply output voltage v_0 is converted into the digital domain by means of an A/D converter 28 and output to an error amplifier 30. The error amplifier 30 compares the digitized supply output voltage v_0 to a reference signal v_{ref} and outputs an
30 error signal v_e . According to an aspect of the present invention, the error amplifier 30 may be realized by means of a differential amplifier.

The digital audio signal v_{audio} is squared with a multiplying unit 32 and

output as the injection signal v_{inj} . The injection signal v_{inj} is provided to a summing unit 34 realized by an adder. By the summing unit 34, the injection signal v_{inj} is superpositioned with the error signal v_e . The output of the summing unit 34, namely the summing signal v_c is compared to a sawtooth signal v_s by means of a comparator 36 to
5 thereby generate a PWM signal that is provided to the gates of the switches 20 and 22 via the drivers 38 and 40. This PWM signal drives the switches 20 and 22 of the buck converter. Due to the above circuit, a very clean supply voltage can be supplied to the switches 20 and 22 since the square of the audio signal is the fundamental component of the load current i_d drawn by the switches 20 and 22 of the amplifier.

10 According to an aspect of the present invention, the power supply ripple pre-compensation circuit 12, the modulator 4, the full bridge 6 and/or the filter 8 are integrated in a module or integrated circuit.

Fig. 3 shows a first exemplary embodiment of a digital amplifier system 80 according to the present invention. The digital amplifier system 80 depicted in Fig. 3
15 is a multi-channel application, where only one supply ripple pre-compensation circuit 60 is used for compensating voltage ripples on a supply voltage supplied to a plurality of bridge circuits 66 and 74 of a plurality of digital amplifiers. In Fig. 3, an audio source 50 outputs a digital audio signal to a channel divider, which separates channels of the digital audio. In the exemplary embodiment depicted in Fig. 3, only two channels are
20 shown. However, the present invention may also be applied to architectures using more than two channels. Therefore, for example the left channel of the audio signal is provided to the upper branch of the amplifier system 80, whereas the right channel is provided to the lower branch of the amplifier system 80 depicted in Fig. 3. Those individual channels of the audio signal are the input signals of the amplifier system 80.
25 The left channel is squared by means of a multiplier 54 and provided to a summing unit 58. Also, the right channel is squared by means of a multiplier 56 and provided to the summing unit 58. The summing unit 58 adds the squared right channel to the squared left channel and provides the summing signal to the supply ripple pre-compensation circuit 60. As already mentioned above the supply ripple pre-compensation circuit 60
30 may have the same arrangement as the supply ripple pre-compensation circuit depicted in Fig. 2. The supply ripple pre-compensation circuit 60 controls the power supply 62 such that the power supply 62 provides a clean supply voltage to the full bridges 66 and

74 of the upper and lower branch of the digital amplifier depicted in Fig. 3. The full bridges 66 and 74 convert the right and left channels, which were pulse-width modulated by means of the modulators 64 and 72, to power outputs provided to loudspeakers 70 and 78 via filters 68 and 76.

5 According to this exemplary embodiment of the present invention, a plurality of input signals (the channels of the audio signal) are combined and used for supply ripple pre-compensation of a plurality of amplifier stages (upper and lower branches of amplifier systems 80). The summing signal provided from the summing unit 58 to the supply ripple pre-compensation circuit 60 represents the total energy
10 being drawn from the power supply. Therefore, a very fast just in time compensation can be realized, allowing for an almost distortion free output signal to the loudspeakers 70 and 78.

 Fig. 4 shows a simplified schematic diagram of a second exemplary embodiment of a digital amplifier system 82 according to the present invention, for
15 converting a plurality of input signals, namely the individual channels of the audio signal to a plurality of power outputs provided to the loudspeakers 70 and 78. In Fig. 4, the same reference numerals as in Fig. 3 are used to designate the same or corresponding elements. As in Fig. 3, only two channels are shown in Fig. 4. However, the present invention may also be applied to systems using more than two channels. In
20 contrast to the embodiment depicted in Fig. 3, only one channel, namely the right channel of the right and left channels of the audio signal is input to the supply ripple pre-compensation circuit 60. In detail, the right channel of the audio signal output by the channel divider 52 is low pass filtered by means of a low pass filter 80, which output signal is provided as input signal to a squaring device 84. This squaring device 84 is
25 provided for estimation of the energy requirements. Instead of being provided between the low pass filter 80 and the supply ripple pre-compensation circuit 60, the squaring device 84 may also be provided according to an aspect of the present invention before the low pass filter 80, i.e. between the channel divider 52 and the low pass filter 80. This exemplary embodiment of the present invention advantageously makes use of the
30 fact that most of the audio signal content is in the low-frequency range of the audio signal. Therefore, the audio signal being fed forward, i.e. being input to the supply ripple pre-compensation circuit 60 advantageously may previously be low pass filtered

by means of the low pass filter 80 to ease the power supply controller without having a loss of performance.

It has to be noted that in spite of the fact that the right channel of Fig. 4 is used as input for the supply ripple pre-compensation circuit 60, also the left one or any
5 other channel of the plurality of channels of an audio signal may be used as input signal for the pre-compensation. This exemplary embodiment of the present invention may advantageously be used for surround sound systems. In such applications, preferably the sub-woofer channel of the audio signal is the channel which is branched out, low pass
10 filtered, squared and then used for pre-compensation by means of the supply ripple pre-compensation circuit. This aspect of the present invention allows for a very good pre-compensation and therefore for a very clean output signal, since the sub-woofer channel transmits the frequency portions with the highest energy.

Furthermore, this exemplary embodiment of the present invention uses the fact that the low frequency content of the audio signal is common to all channels.
15 Therefore, according to the present invention, it is sufficient to only use one low pass filtered channel for pre-compensation of all amplified channels. Advantageously, according to the present invention, this allows to reduce the overall size and cost of the amplifier. As already mentioned above, the elements depicted in Figs. 3 and 4 except for the audio source 50 and/or the filters 68 and 76 and the loudspeakers 70 and 78 may
20 be integrated in one integrated circuit IC or module.